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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/654,875	09/05/2000	Isao Nojiri	50006-073	7618
7590	07/13/2005		EXAMINER	
McDermott Will & Emery 600 13th Street N W Washington, DC 20005-3096			PAREKH, NITIN	
			ART UNIT	PAPER NUMBER
			2811	
			DATE MAILED: 07/13/2005	

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary	Application No.	Applicant(s)
	09/654,875	NOJIRI ET AL.
Examiner	Art Unit	
Nitin Parekh	2811	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133).

Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

1) Responsive to communication(s) filed on 01 June 2005.

2a) This action is FINAL. 2b) This action is non-final.

3) Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

4) Claim(s) 10 and 13-22 is/are pending in the application.
4a) Of the above claim(s) _____ is/are withdrawn from consideration.

5) Claim(s) 10 and 13-21 is/are allowed.

6) Claim(s) 22 is/are rejected.

7) Claim(s) _____ is/are objected to.

8) Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

9) The specification is objected to by the Examiner.

10) The drawing(s) filed on 05 September 2000 is/are: a) accepted or b) objected to by the Examiner.

Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).

Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).

11) The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
a) All b) Some * c) None of:
1. Certified copies of the priority documents have been received.
2. Certified copies of the priority documents have been received in Application No. _____.
3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

1) Notice of References Cited (PTO-892)
2) Notice of Draftsperson's Patent Drawing Review (PTO-948)
3) Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)
Paper No(s)/Mail Date _____

4) Interview Summary (PTO-413)
Paper No(s)/Mail Date. _____
5) Notice of Informal Patent Application (PTO-152)
6) Other: _____

DETAILED ACTION

Request for Continued Examination

1. Applicant's submission of RCE filed on 06/01/05 has been entered. An action on the RCE follows.
2. The amendment filed on 06/01/2005 has been entered.

Claim Rejections - 35 USC § 102

3. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.

4. Claim 22 is rejected under 35 U.S.C. 102(e) as being anticipated by Fukui et al. (US Pat. 6100594).

Regarding claim 17, Fukui et al. disclose a semiconductor device (Fig. 7B) comprising:

- a wiring substrate/circuit board (not numerically referenced in Fig. 8b; see 3/4/5 in Fig. 4/6a)

- a first semiconductor chip (2 in Fig. 7b) having a first electrode pad (see third pad 17b from left on chip 2 in Fig. 7b)
- a second semiconductor chip (1 in Fig. 7b) having a second electrode pad (see third pad 17a from left on chip 1 in Fig. 7b)
- the wiring substrate/circuit board has a fourth electrode pad (see fourth pad 13 from left in Fig. 7b)
- a first bonding wire (see 7 between the two chips in Fig. 7b) connecting the first electrode pad and the second electrode pad
- a second bonding wire (see 7 between the first chip and the wiring substrate/circuit board in Fig. 7b) connecting the second electrode pad and the fourth electrode pad

(Fig. 7b; Fig. 4 and 6a; Col. 10, lines 30-50).

Fukui et al. further disclose the second chip having the second electrode pad configuration (see 17a in Fig. 9b) such that it comprises a second pad portion and a third pad portion (see left and right pad portions being connected in Fig. 9b) wherein a connecting wire (see middle wiring portion between the second and third pad portions in Fig. 9b) electrically connects the second and third pad portions, such configuration preventing the wires being too close to each other (Col. 10, lines 30-67). Furthermore, the second pad portion is formed on a top surface of the second semiconductor chip, towards/beside its one edge/lateral edge, and the third pad portion is formed

towards/beside its another edge/vertical edge and such configuration further provides additional bonding area such that the second bonding wire can be connected between the third pad portion and the fourth electrode pad.

Allowable Subject Matter

4. Claims 10 and 13-21 are allowed.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Nitin Parekh whose telephone number is 571-272-1663. The examiner can normally be reached on 09:00AM-05:30PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's acting supervisor, Steven Loke can be reached on 571-272-1657. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAN or Public PAG. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAG system, contact the Electronic Business Center

(EBC) at 866-217-9197 (toll-free). Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is 703-308-0956.



NITIN PAREKH

NP

07-09-05

PRIMARY EXAMINER

TECHNOLOGY CENTER 2800